

Action Taken Report

Course Feedback by Students

Name of the Faculty	FET	
Programme	B.Tech. in Electronics and Communication Engineering	
Period for which feedback taken	Academic Year 2024-25 (Odd Mid-Semester/Year) ongoing	
Type of Feedback	Course Feedback by Students on ongoing semester/year cou	
Semester/Year covered	Odd 3 rd semester	
ame of the Faculty feedback Mr. Ambresh pordinator		

SI. No.	Course on which feedback obtained	Semester/ Year	Critical Observations from feedback	Action Taken with target date	Any other remarks/c omments
1	Essence of Indian Knowledge (TSN201A)	3 rd semester / 2 nd Year			-
2	Introduction to VLSI Life Cycle (ECC206A).	3 nd semester /	-		
3	Engineering Mathematics-3 (MTF201A)	semester /	-		-
4	Digital Logic Design (ECC205A)	semester /	-		-
5	Signals and System (ECC202A)	3 rd semester / 2 rd Year	-		
6	Electronic Circuits (ECC204A)	36 ^d semester / 2 Year	She is getting too personal with some particular students and she demotivates us some times.	This has been brought to the notice of HOD. HOD have discussed with course leader and students.	

Signature with Date of Feedback Coordinator	(Also
Signature with Date of Head of Department	Dept. of Electronics & Communication Engra Faculty of Engineering & Technology M.S. Ramaiah University of Applied Sciences # 470-P, Peenya Industrial Area, 4th Phase,
Signature with Date of Respective Dean	Bangalore-560058. Ph: 080-49065555 Dean Faculty of Engineering & Technology M.S. Ramaiah University of Applied Sciences



Action Taken Report

Course Feedback by Students

Name of the Faculty	FET	
Programme	B.Tech. in Electronics and Communication Engineering	
Period for which feedback taken	Academic Year 2024-25 (Odd End-Semester/Year) ongoing	
Type of Feedback	Course Feedback by Students on ongoing semester/year courses	
Semester/Year covered	Odd 3 rd semester	
Name of the Faculty feedback Coordinator	Mr. Ambresh	

SI. No.	Course on which feedback obtained	Semester/ Year	Critical Observations from feedback	Action Taken with target date	Any other remarks/c omments
1	Essence of Indian Knowledge (TSN201A)	3 rd semester / 2 nd Year	A proper notes should be provide. Little more interactive sessions should be taken and more videos based explanation is expected.	This have brought to notice of course leader through HOD, Dean.	-
2	Introduction to VLSI Life Cycle (ECC206A)	semester	-	-	•
3	Engineering Mathematics-3 (MTF201A)	3 rd semester / 9 rd Year	-		<u>.</u>
4	Digital Logic Design (ECC205A)	3 nd semester / 2 nd Year	-		-
5	Signals and System (ECC202A)	3 rd semester /2 rd Year	-		
6	Electronic Circuits (ECC204A)	semester / 2 Year	•		

Signature with Date of Feedback Coordinator	Shore 20.01-2025
Signature with Date of Head of Department	Dept. of Electronics & Communication Engg. Faculty of Engineering & Technology M.S. Ramaiah University of Applied Sciences # 470-P, Peenya Industrial Area, 4th Phase,
Signature with Date of Respective Dean 18.2.25 Dean	Bangalore-560058. Ph: 080-49065555

Faculty of Engineering & Technology
M.S. Ramajah University of Applied Sciences
Bangalore - 560 658.