M. S. Ramaiah School of Advanced Studies

P atents 201 0-20
11

1. Dipayan Mazumdar, Cyril Prasanna Raj and Brahmananda Reddy, "*Wavelet Transformation Using Multicore Processors*" Indian Patent Application:3635/CHE/2010, November 30, 2010 International Patent Application PCT/IB2011/050167

2. Dipayan Mazumdar and Cyril Prasanna Raj, "*Optimized Multichannel switched network interface for Multicore Processor*" Indian Patent Application: 2040/CHE/2010, July 16, 2010, International Patent Application PCT/IB2010/054706

3. Dipayan Mazumdar, "*Symmetric Matrix Inversion System and Method*", Indian Patent Application: 2473/CHE/2010, August 25, 2010

4. Dipayan Mazumdar and Cyril Prasanna Raj, "*An apparatus for computation of wavelet transform using systolic arrays with low gate count, low memory, low I/O count, and energy requirements*" Indian Patent Application: 3635/CHE/2010, November 30, 2010, International Patent Application PCT/IB2011/050167, January 14, 2011

5. Dipayan Mazumdar and Govind R Kadambi, "*Method and Apparatus for Direct Digital Synthesis of Signals using Taylor Series Expansion"* Indian Patent Application: 2489/CHE/2010, October 19, 2010, International Patent Application PCT/IB2010/054721