

Patents Awarded to MSRUAS

1. Data Interface Circuit - **U.S. Patent 8,451, 147 May 28, 2013**
Inventors: **Mazumdar, Dipayan and Raj, Cyril P**
2. Method and Apparatus for Direct Digital Synthesis of Signals Using Taylor Series Expansion - **U.S. Patent 8,570,203 October 29, 2013**
Inventors: **Mazumdar, Dipayan and Kadambi, Govind Rangaswamy**
3. Mobile Ration Shop - **Indian Patent 2,60,642 February 27, 2014** Inventors:
R. Ratheesh Kumar, S. Subbaramu and Vignesh Ravichandran
4. Data Interface Circuit – **Korean Patent 10- 1451254 October 15, 2014** Inventors: **Mazumdar, Dipayan and Raj, Cyril P**
5. "Wavelet Transformation Using Multicore Processors" –
Japan Patent 5640157 October 31 , 2014
Inventors: **Dipayan Mazumdar, Cyril Prasanna Raj and Brahmananda Reddy**
6. Adhoc Network – **Japan Patent 5687773 January 30, 2015**
Inventors: **Govind R. Kadambi, Rinki Sharma and P. Shiva Kumar**
7. "Wavelet Transformation Using Multicore Processors" –
Korean Patent 10-1490153 February 5, 2015
Inventors: **Dipayan Mazumdar, Cyril Prasanna Raj and Brahmananda Reddy**
8. Adhoc Network – **Korean Patent 10-1500477 March 3, 2015**
Inventors: **Govind R. Kadambi, Rinki Sharma and P. Shiva Kumar**